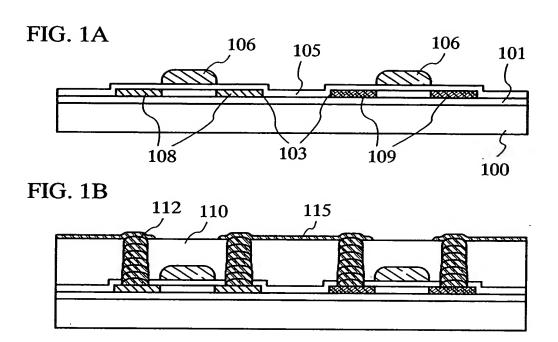
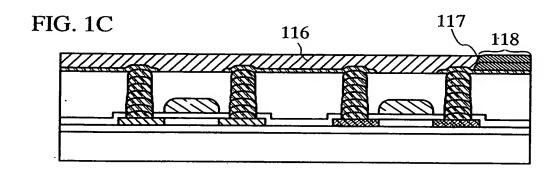
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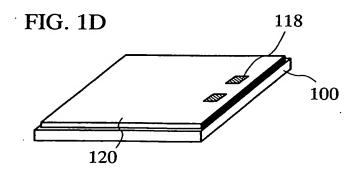
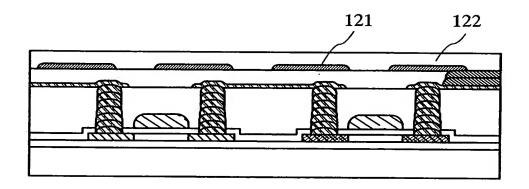
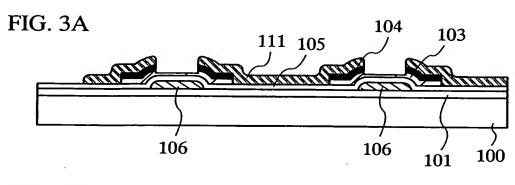
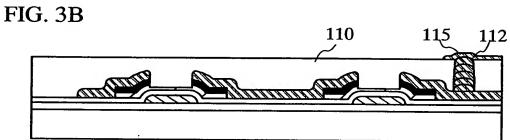


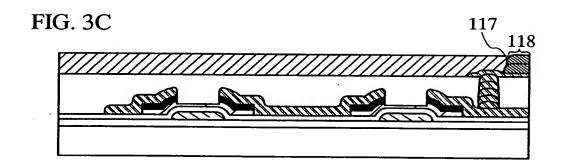
FIG. 2



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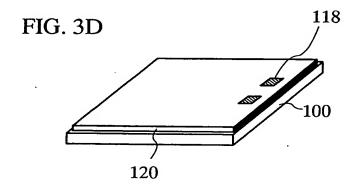
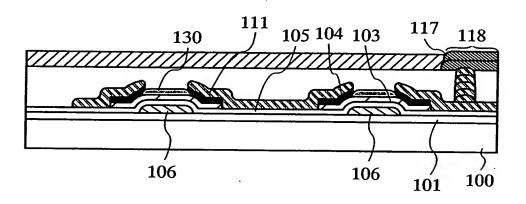
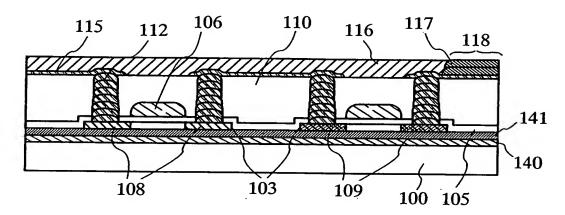


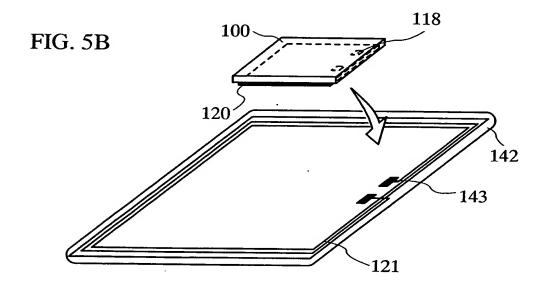
FIG. 4

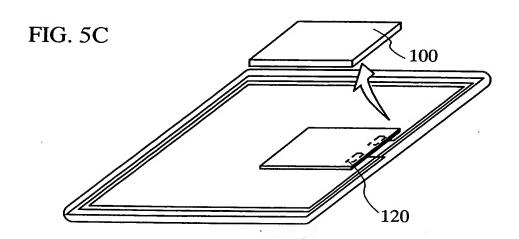


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FIG. 5A

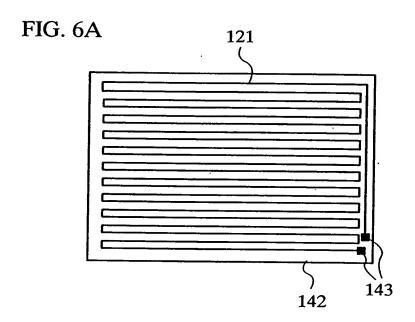


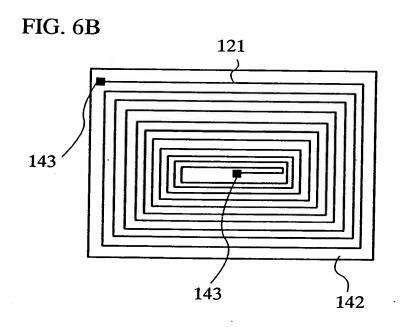




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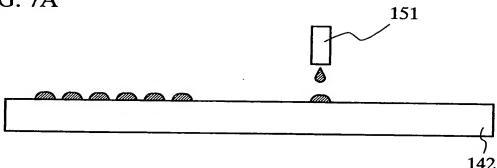


FIG. 7B

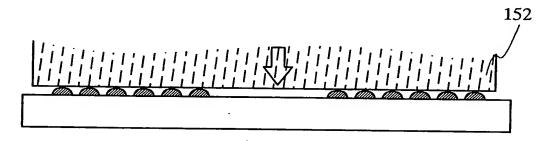
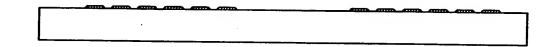
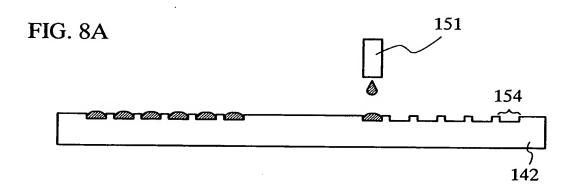
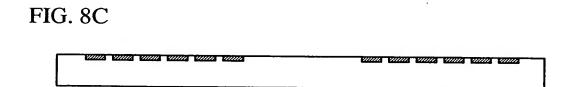


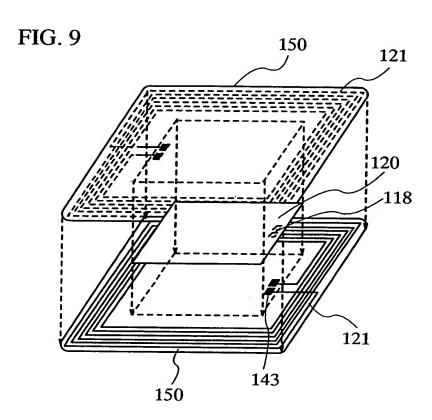
FIG. 7C.











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FIG. 10A

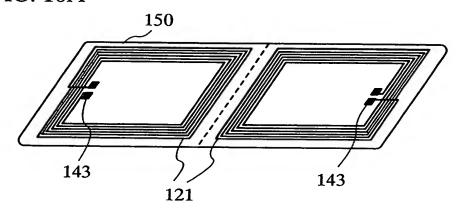
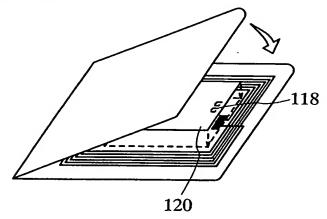


FIG. 10B



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FIG. 11A

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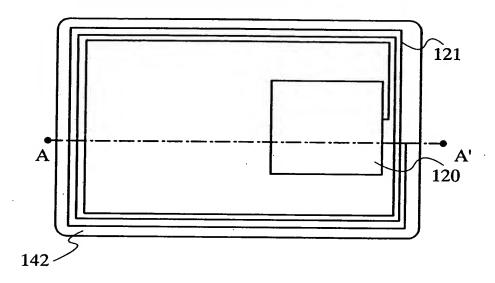


FIG. 11B

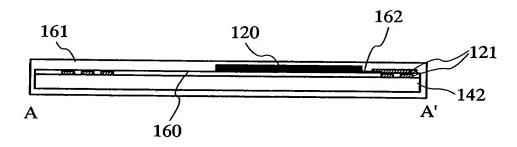


FIG. 11C

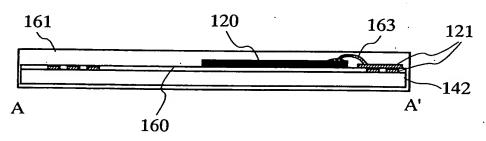


FIG. 12A

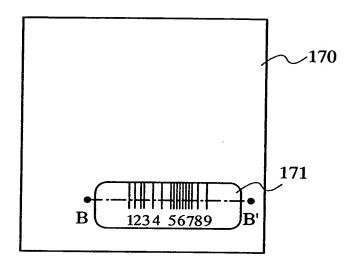


FIG. 12B

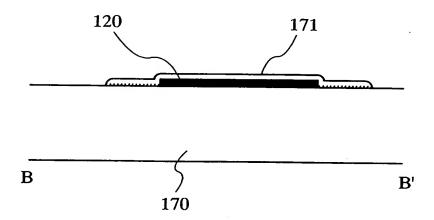
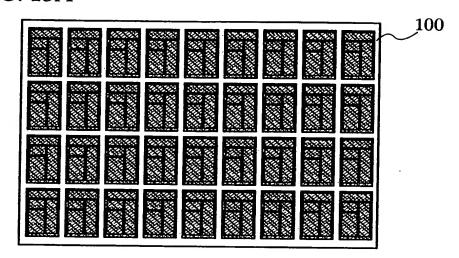
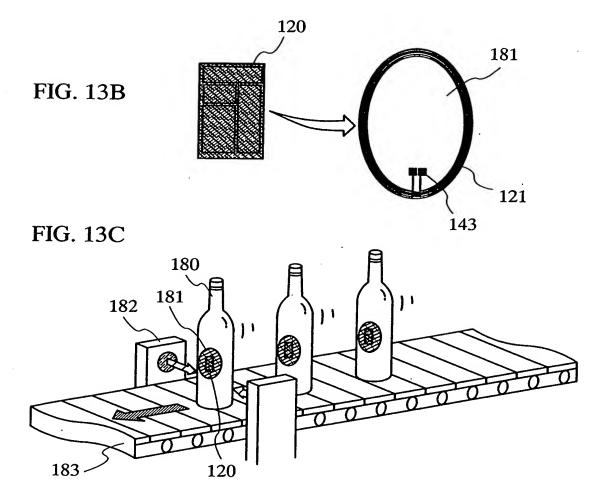


FIG. 13A





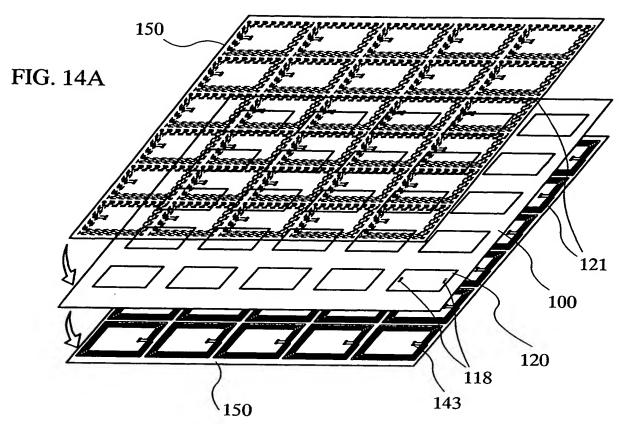


FIG. 14B

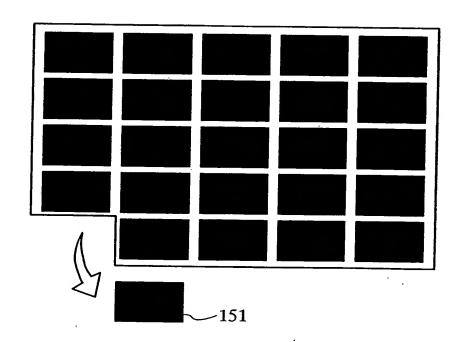
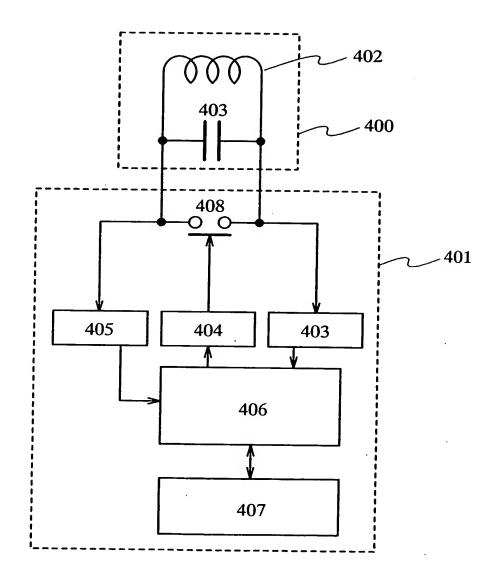


FIG. 15



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EXPLANATION OF REFERENCE

100: substrate, 101: base film, 101: base film, 102: amorphous semiconductor film, 103: semiconductor film, 105: gate insulating film, 106: gate electrode, 108a: conductive film, 108b: conductive film, 200: film thickness, 108: impurity region, 109: impurity region, 110: insulating film, 112: wiring, 122: wiring, 115: conductive film, 116: insulating film, 110: interlayer insulating film, 117: conductive film, 118: connection terminal portion, 121: antenna, 122: insulating film, 120: thin film integrated circuit, 104: semiconductor film, 111: drain electrode, 130: protective film, 140: metal, 140: metal film, 141: separation layer, 143: connection terminal portion, 142: substrate, 100: substrate, 142: substrate, 143: connection terminal, 151: nozzle, 152: pressurizing unit, 154: opening portion, 150: antenna substrate, 100: large substrate, 143: connection terminal portion, 151: ID chip, 400: antenna, 402: antenna coil, 403: capacitor element, 401: thin film integrated circuit, 409: demodulation 404: modulation circuit, 405: rectification circuit, microprocessor, 407: memory, 408: switch,160: insulating film, 162: connecting wiring, 161: film,163: wire, 170:product, 171: bar codes, 181: label, 180: bottle, 183: belt conveyor, 182: reader device.